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S/N 10/829,559

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Asher Hazanchuk	Examiner:	Do, Chat C
Serial No.:	10/829,559	Group Art Unit:	2193
Filed:	April 22, 2004	Docket No.:	ALT.P030 (A01252)
		Confirmation No.	6357
Title:	Method and Apparatus for Implementing a Multiplier Utilizing Digital Signal Processor Block Memory Extension		

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir,

In response to the Office Action mailed October 28, 2010 and the Advisory Action mailed January 4, 2011, Applicants respectfully request the Examiner to consider the following remarks. A Request for Continued Examination is being filed herewith.

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